

Maharshi Karve Stree Shikshan Samtha's
Cummins College of Engineering for Women, Karvenagar Pune – 411052
 (An Autonomous Institute Affiliated to SPPU)
 S. Y. B. Tech: E & TC Department of Electronics and Telecommunication Engg Academic Year: 2017-18

CLASS TIME-TABLE Div: C W.E.F. 17/07/2017

SEM-I	TIME →	1.00-2.00 pm	2.00-3.00 pm	3.00-4.00 pm	4.00-5.00 pm	5.00-5.30 pm	5.30-6.30 pm	6.30-7.30 pm
	DAY ↓							
	MON	EDC(RP) / DE(RRB) / DS(TK) C1(Analog) / C2(FOC) / C4(OOP)	↕	NT	DS	Short Break		
	TUE	M-III TUT C3(2)(SG) EDC(RP) / DE(RRB) / DS(TK) C4(Analog) / C1(FOC) / C3(OOP)	↕	NT	DE			
	WED	EDC(RP) / DE(RRB) / DS(TK) C3(Analog) / C4(FOC) / C2(OOP)	↕	EDC	M-III		DE	
	THU	NT TUT C1(COMP 1) (MKP) EDC(RP) / DE(RRB) / DS(TK) C2(Analog) / C3(FOC) / C1(OOP)	↕	EDC(TUT C2(RP)) C3(Analog) / C1(OOP)			NT	M-III
	FRI	NT TUT C4(COMP 1) (MKP) EDC(RP) / EDC(RTS) / DS(TK) / DS(SAP) C1(Analog) / C2(PE) / C4(OOP) / C3(COMP 1)	↕	EDC(TUT C3(RP)) DE TUT C1(2)(RRB)	EDC(TUT C3(3)(RRB)) DE TUT C1(2)(RRB)		EDC	M-III TUT C2(5) (SG)

Name of the Class Teacher: Prof. T. S. Kadam

Subject Name	Faculty Name	Batches For Practical	Name of Mentors
EDC: Electronics Devices and Circuits	Prof. R. Pawar	C1	Prof. R. T. Suryawanshi
DE: Digital Electronics	Prof. R. R. Borhade	C2	Prof. K. S. Joshi
DS: Data Structures	Prof. T. S. Kadam	C3	Prof. P. C. Shenolikar
NT: Network Theory	Prof. K. S. Joshi	C4	Prof. T. S. Kadam
M-III: Engineering Mathematics-III	Prof. S. Gambhir		
Venue of Class Room: 5			

Prof. S. G. Dube
 (Time Table In-Charge)

Dr. P. Mukherji
 (H.O.D., E & TC)

(Signature)
 Dr. P. C. Khabbete
 (Director)